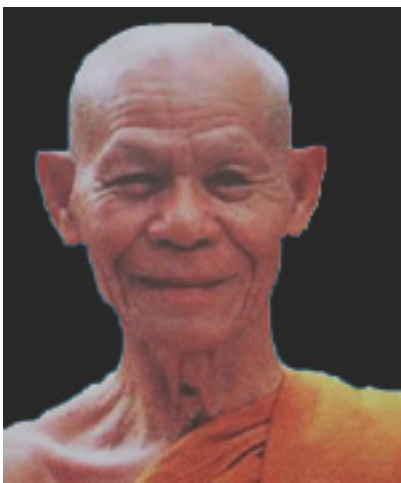




The diagram illustrates the hierarchical structure of a 16-bit bus. It starts with a single 16-bit bus at the top, which is then divided into two 8-bit halves. Each 8-bit half is further divided into two 4-bit quarters. Each 4-bit quarter is divided into two 2-bit eighths. Finally, each 2-bit eighth is divided into two 1-bit sixteenths. The bottom row shows the final 1-bit signals, with the last 8 bits highlighted in pink to represent the 'lower 8 bits' mentioned in the text.

[illegible]

2 / 17

[illegible]



[illegible]

[illegible]

6 / 17

[illegible]

8 / 17

[illegible]

[illegible]

The diagram consists of 10 rows of small squares. The first row has 10 squares, and the subsequent rows have 10 squares each. The squares are arranged in a grid, with some squares highlighted in black. The highlighted squares are located at the following positions (row, column): (1, 1), (1, 2), (1, 3), (1, 4), (1, 5), (1, 6), (1, 7), (1, 8), (1, 9), (1, 10), (2, 1), (2, 2), (2, 3), (2, 4), (2, 5), (2, 6), (2, 7), (2, 8), (2, 9), (2, 10), (3, 1), (3, 2), (3, 3), (3, 4), (3, 5), (3, 6), (3, 7), (3, 8), (3, 9), (3, 10), (4, 1), (4, 2), (4, 3), (4, 4), (4, 5), (4, 6), (4, 7), (4, 8), (4, 9), (4, 10), (5, 1), (5, 2), (5, 3), (5, 4), (5, 5), (5, 6), (5, 7), (5, 8), (5, 9), (5, 10), (6, 1), (6, 2), (6, 3), (6, 4), (6, 5), (6, 6), (6, 7), (6, 8), (6, 9), (6, 10), (7, 1), (7, 2), (7, 3), (7, 4), (7, 5), (7, 6), (7, 7), (7, 8), (7, 9), (7, 10), (8, 1), (8, 2), (8, 3), (8, 4), (8, 5), (8, 6), (8, 7), (8, 8), (8, 9), (8, 10), (9, 1), (9, 2), (9, 3), (9, 4), (9, 5), (9, 6), (9, 7), (9, 8), (9, 9), (9, 10), (10, 1), (10, 2), (10, 3), (10, 4), (10, 5), (10, 6), (10, 7), (10, 8), (10, 9), (10, 10).

[illegible][illegible][illegible]

13 / 17



[illegible]

□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □

33

